

**Listing of Claims**

1           1.       (Previously Presented) An integrated circuit for providing a switchover from a  
2 primary power source to a secondary power source, the integrated circuit comprising:

3                 a power source switchover circuit for receiving a supply level of the primary  
4 power source and generating (i) a reference signal based on the supply level of the primary  
5 power source and (ii) an indication signal representative of the supply level of the primary power  
6 source, the power source switchover circuit having a plurality of input terminals, a first input  
7 terminal electrically coupled to the primary power source and a second input terminal electrically  
8 coupled to a substrate of the integrated circuit, the power source switchover circuit further  
9 having a plurality of output terminals;

10                a comparator, electrically coupled to the power source switchover circuit, for  
11 indicating that the indication signal has decreased below the reference signal, the comparator  
12 having a first and a second input terminal connected to a first and a second output terminal of the  
13 power source switchover circuit, respectively, the comparator further having an output terminal;

14                a forced power source switchover circuit for detecting that the supply level of the  
15 primary power source drops below a predefined threshold level, the forced power source  
16 switchover circuit having at least one input terminal and at least one output terminal, one input  
17 terminal being electrically connected to the primary power source; and

18                a switchover circuit for initiating a switchover operation based upon the  
19 indication from the comparator or upon an indication that the forced power source switchover  
20 circuit detects that the supply level of the primary power drops below the predefined threshold

21 level, the switchover circuit having a first input terminal connected to the output terminal of the  
22 comparator and a second input terminal connected to one output terminal of the forced power  
23 source switchover circuit.

1           2.       (Previously Presented) The integrated circuit according to claim 1, wherein the  
2 forced power source switchover circuit is reactive to the supply level of the primary power  
3 source transitioning from a steady-state supply level to below the predefined threshold level, the  
4 supply level of the primary power source transitioning faster than a predetermined negative rate  
5 of change.

1           3.       (Previously Presented) The integrated circuit according to claim 2, wherein the  
2 negative rate of change of the supply level is approximately 150 microseconds from steady state  
3 to below the predefined threshold level.

1           4.       (Previously Presented) The integrated circuit according to claim 1, further  
2 comprising circuitry connected to the power source switchover circuit for selectively adjusting  
3 the indication signal produced by the comparator.

1           5.       (Previously Presented) The integrated circuit according to claim 1, further  
2 comprising circuitry connected to the forced power source switchover circuit for selectively  
3 adjusting the predefined threshold level.

1           6.       (Previously Presented) The integrated circuit according to claim 5, further  
2 comprising a plurality of input terminals for receiving input signals to configure the switchover  
3 circuit to selectively adjust the predefined threshold level.

1           7.       (Original) The integrated circuit according to claim 1, further comprising a delay  
2 circuit for providing a time duration for the substrate of the integrated circuit to settle from a first  
3 voltage potential to a second voltage potential upon the switchover from the secondary power  
4 source to the primary power source.

1           8.       (Previously Presented) The integrated circuit according to claim 1, wherein the  
2 predefined threshold level associated with the forced power source switchover circuit is below  
3 the crossing level of the supply level of the primary power source and the reference signal.

1           9.       (Previously Presented) A method for performing a power source switchover from  
2 a primary power source to a secondary power source, the method comprising the steps of:  
3               detecting that a supply level being received from the primary power source  
4 decreases below a predefined threshold level from a steady-state operating level, the supply level  
5 of the primary power source transitioning faster than a predetermined negative rate of change,  
6 said detecting being based on (i) the supply level of the primary power source and (ii) a voltage  
7 level of a substrate of an integrated circuit;

8           asserting a signal indicating to switch from the primary power source to the  
9   secondary power source upon detecting that the supply level of the primary power source has  
10   decreased below the predefined threshold level;

11           detecting the signal indicating to switch from the primary power source to the  
12   secondary power source; and

13           switching from the primary power source to the secondary power source based  
14   upon detecting the signal indicating to switch from the primary power source to the secondary  
15   power source.

1           10.   (Original) The method according to claim 9, wherein the primary power source is  
2   an external power source.

1           11.   (Original) The method according to claim 9, wherein the secondary power source  
2   is a battery.

1           12.   (Original) The method according to claim 9, wherein the predefined threshold  
2   level is below 2.5 volts.

1           13.   (Previously Presented) The method according to claim 9, wherein the negative  
2   rate of change of the supply level is approximately 150 microseconds from steady state to below  
3   the predefined threshold level.

1           14.   (Previously Presented) The method according to claim 9, further comprising the  
2 steps of:  
3               receiving the supply level being delivered from the primary power source;  
4               receiving the a voltage potential of the substrate of an integrated circuit;  
5               generating a reference signal and an indication signal based on the received  
6 supply level and voltage potential; and  
7               producing a compare signal indicative of the relative values between the reference  
8 signal and indication signal.

1           15.   (Original) The method according to claim 14, further comprising the steps of:  
2               receiving the compare signal and the signal indicating to force the power source  
3 switchover;  
4               determining an occurrence of a transition of either the compare signal or the  
5 signal indicating to force the power source switchover; and  
6               initiating a switch from the primary power source to the secondary power source  
7 upon the determination of the occurrence of a transition of the compare signal or the signal  
8 indicating to force the power source switchover.

1           16.   (Previously Presented) A circuit comprising:  
2               a first detection circuit for detecting a supply level of a primary power source  
3 decreasing from a steady-state supply level to a predefined threshold level faster than a

4 predetermined negative rate of change, the first detection circuit basing the detection on the  
5 supply level and a voltage level of a substrate of the circuit, and further generating at least one  
6 signal in response to the supply level decreasing to the predefined threshold level; and  
7 a first switching circuit for switching from the primary power source to a  
8 secondary power source in response to the at least one signal.

1 17. (Previously Presented) The circuit according to claim 16, wherein the primary  
2 power source is an external power source relative to the circuit.

1 18. (Original) The circuit according to claim 16, wherein the secondary power source  
2 is a battery.

1 19. (Previously Presented) The circuit according to claim 16, wherein the  
2 predetermined negative rate of change of the supply\_level is approximately 150 microseconds  
3 from steady state to below the predefined threshold level.

1 20. (Original) The circuit according to claim 16, further comprising circuitry for  
2 adjusting a response time for the first detection circuit.

1           21.     (Previously Presented) The circuit according to claim 16, further comprising:  
2                     a second detection circuit for detecting a voltage threshold of the first detection  
3 circuit and the supply level being received from the primary power source, the second detection  
4 circuit producing a plurality of signals based upon the detecting;  
5                     a comparator in communication with the second detection circuit and operable to  
6 compare at least two of the signals produced by the second detection circuit and produce a  
7 comparison signal; and  
8                     wherein the first switching circuit is operable to switch from the primary power  
9 source to the secondary power source in response to the comparison signal.

1           22.     (Original) The circuit according to claim 21, further comprising circuitry for  
2 selectively responding to the first and the second detection circuits.

1           23.     (Previously Presented) The circuit according to claim 21, further comprising an  
2 input terminal coupled to a circuit operable to selectively disable a switchover from the primary  
3 to the secondary power source.

1           24 - 41.     (Canceled)